

Three quadrant triacs guaranteed commutation

BTA208X series D, E and F

GENERAL DESCRIPTION

Passivated guaranteed commutation triacs in a full pack, plastic envelope intended for use in motor control circuits or with other highly inductive loads. These devices balance the requirements of commutation performance and gate sensitivity. The "sensitive gate" E series and "logic level" D series are intended for interfacing with low power drivers, including micro controllers.

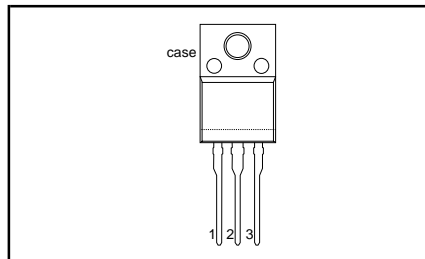
QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	MAX.	UNIT
V_{DRM}	Repetitive peak off-state voltages	600D 600E 600F 600	- 800E 800F 800	V
$I_{T(RMS)}$	RMS on-state current	8	8	A
I_{TSM}	Non-repetitive peak on-state current	65	65	A

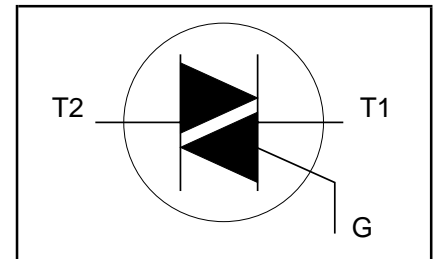
PINNING - SOT186A

PIN	DESCRIPTION
1	main terminal 1
2	main terminal 2
3	gate
case	isolated

PIN CONFIGURATION



SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.		UNIT
V_{DRM}	Repetitive peak off-state voltages		-	-600 600 ¹	-800 800	V
$I_{T(RMS)}$	RMS on-state current	full sine wave; $T_{hs} \leq 73\text{ }^\circ\text{C}$	-	8		A
I_{TSM}	Non-repetitive peak on-state current	full sine wave; $T_j = 25\text{ }^\circ\text{C}$ prior to surge	-	65		A
I^2t	I^2t for fusing	$t = 20\text{ ms}$	-	72		A
dl_T/dt	Repetitive rate of rise of on-state current after triggering	$t = 16.7\text{ ms}$	-	21		A ² s
		$t = 10\text{ ms}$	-	100		A/ μs
I_{GM}	Peak gate current	$I_{TM} = 12\text{ A}; I_G = 0.2\text{ A}; dl_G/dt = 0.2\text{ A}/\mu\text{s}$	-	2		A
P_{GM}	Peak gate power		-	5		W
$P_{G(AV)}$	Average gate power	over any 20 ms period	-	0.5		W
T_{stg}	Storage temperature		-40	150		$^\circ\text{C}$
T_j	Operating junction temperature		-	125		$^\circ\text{C}$

¹ Although not recommended, off-state voltages up to 800V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 6 A/ μs .

Three quadrant triacs guaranteed commutation

BTA208X series D, E and F

ISOLATION LIMITING VALUE & CHARACTERISTIC

$T_{hs} = 25\text{ °C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_{isol}	R.M.S. isolation voltage from all three terminals to external heatsink	$f = 50\text{-}60\text{ Hz}$; sinusoidal waveform; R.H. $\leq 65\%$; clean and dustfree	-	-	2500	V
C_{isol}	Capacitance from T2 to external heatsink	$f = 1\text{ MHz}$	-	10	-	pF

THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$R_{th\ j\text{-}hs}$	Thermal resistance junction to heatsink	full or half cycle with heatsink compound	-	-	4.5	K/W
$R_{th\ j\text{-}a}$	Thermal resistance junction to ambient	without heatsink compound in free air	-	55	6.5	K/W

STATIC CHARACTERISTICS

$T_j = 25\text{ °C}$ unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.			UNIT
				...D	...E	...F	
I_{GT}	Gate trigger current ²	BTA208X- $V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$ T2+ G+ T2+ G- T2- G-	-	5	10	25	mA
			-	5	10	25	mA
			-	5	10	25	mA
			-	5	10	25	mA
I_L	Latching current	$V_D = 12\text{ V}$; $I_{GT} = 0.1\text{ A}$ T2+ G+ T2+ G- T2- G-	-	15	25	30	mA
			-	25	30	40	mA
			-	25	30	40	mA
			-	25	30	40	mA
I_H	Holding current	$V_D = 12\text{ V}$; $I_{GT} = 0.1\text{ A}$	-	15	25	30	mA
V_T	On-state voltage	$I_T = 10\text{ A}$	-	1.65			V
V_{GT}	Gate trigger voltage	$V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$	-	1.5			V
		$V_D = 400\text{ V}$; $I_T = 0.1\text{ A}$; $T_j = 125\text{ °C}$	0.25	-			V
I_D	Off-state leakage current	$V_D = V_{DRM(max)}$; $T_j = 125\text{ °C}$	-	0.5			mA

² Device does not trigger in the T2-, G+ quadrant.

Three quadrant triacs guaranteed commutation

BTA208X series D, E and F

DYNAMIC CHARACTERISTICS

$T_j = 25\text{ °C}$ unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.			MAX.	UNIT
			...D	...E	...F		
dV_D/dt	Critical rate of rise of off-state voltage	BTA208X- $V_{DM} = 67\% V_{DRM(max)}$; $T_j = 110\text{ °C}$; exponential waveform; gate open circuit	20	60	70	-	V/ μ s
di_{com}/dt	Critical rate of change of commutating current	$V_{DM} = 400\text{ V}$; $T_j = 125\text{ °C}$; $I_{T(RMS)} = 8\text{ A}$; $dV_{com}/dt = 10\text{ V}/\mu\text{s}$; gate open circuit	2	5	14	-	A/ms
di_{com}/dt	Critical rate of change of commutating current	$V_{DM} = 400\text{ V}$; $T_j = 125\text{ °C}$; $I_{T(RMS)} = 8\text{ A}$; $dV_{com}/dt = 0.1\text{ V}/\mu\text{s}$; gate open circuit	6	10	20	-	A/ms

Three quadrant triacs guaranteed commutation

BTA208X series D, E and F

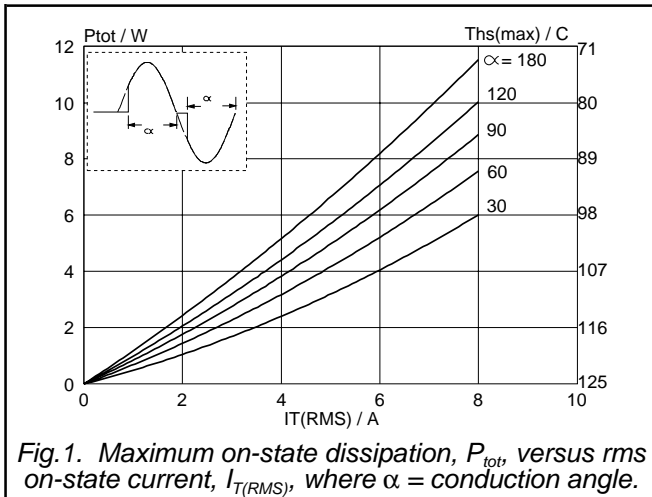


Fig. 1. Maximum on-state dissipation, P_{tot} , versus rms on-state current, $I_{T(RMS)}$, where α = conduction angle.

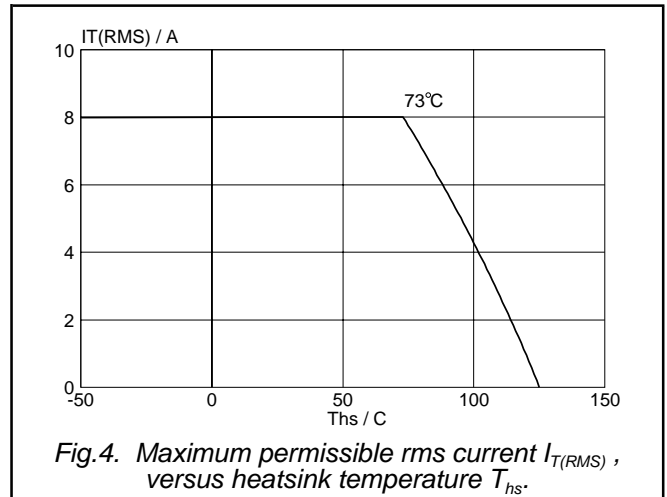


Fig. 4. Maximum permissible rms current $I_{T(RMS)}$, versus heatsink temperature T_{hs} .

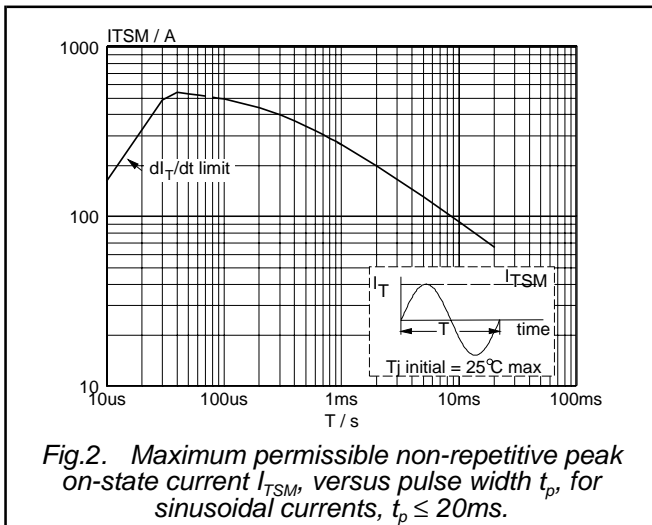


Fig. 2. Maximum permissible non-repetitive peak on-state current I_{TSM} , versus pulse width t_p , for sinusoidal currents, $t_p \leq 20ms$.

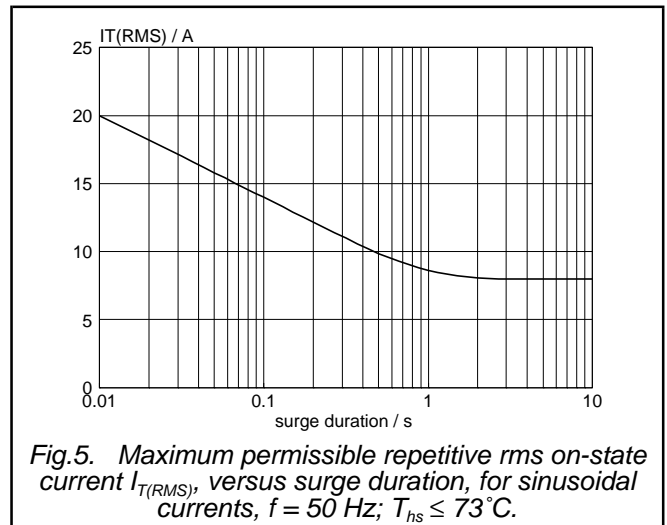


Fig. 5. Maximum permissible repetitive rms on-state current $I_{T(RMS)}$, versus surge duration, for sinusoidal currents, $f = 50 Hz$; $T_{hs} \leq 73^\circ C$.

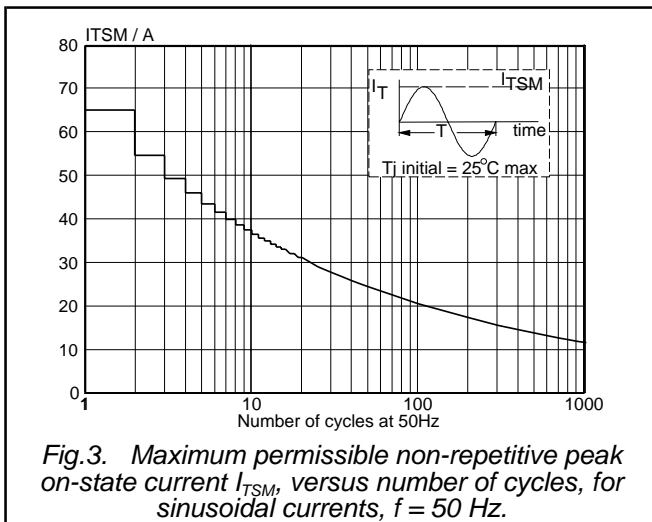


Fig. 3. Maximum permissible non-repetitive peak on-state current I_{TSM} , versus number of cycles, for sinusoidal currents, $f = 50 Hz$.

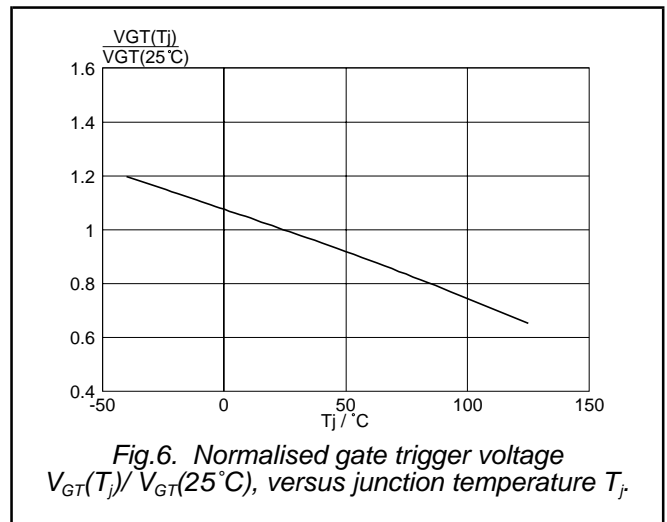
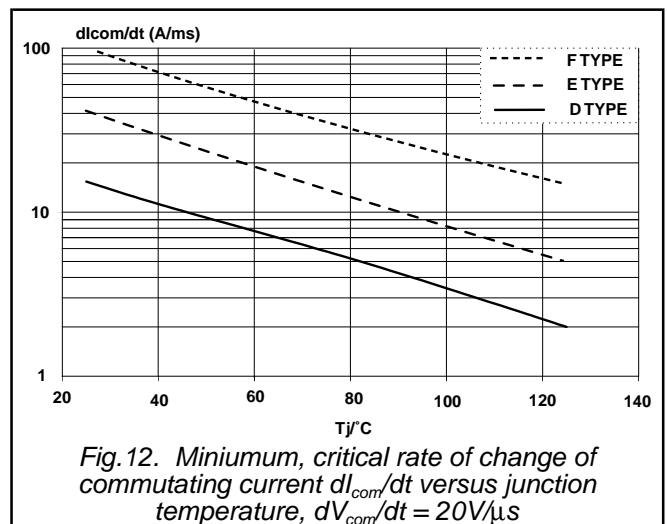
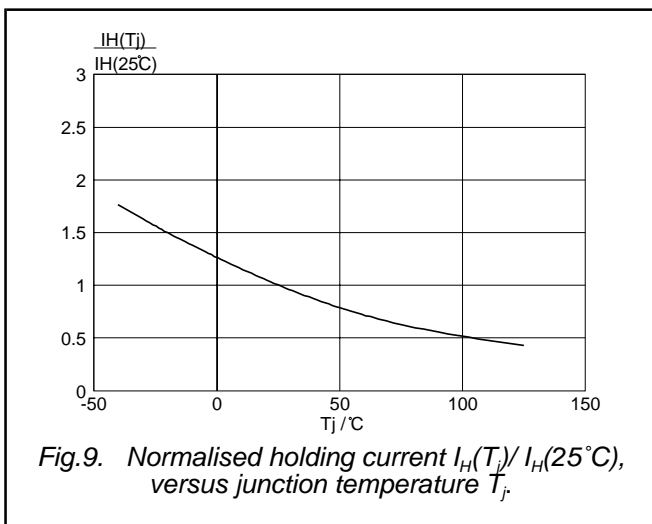
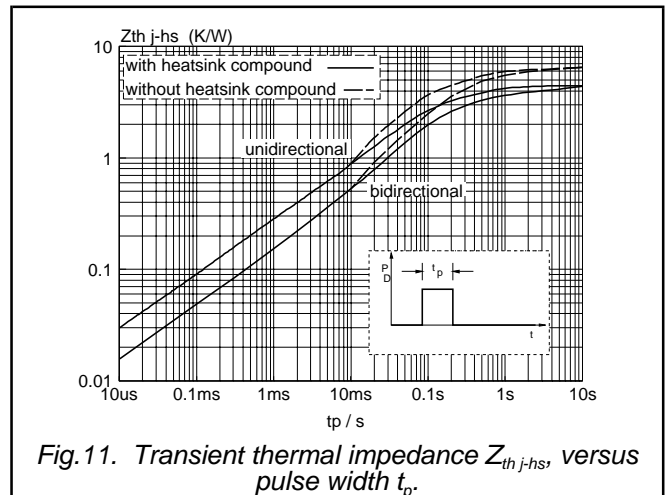
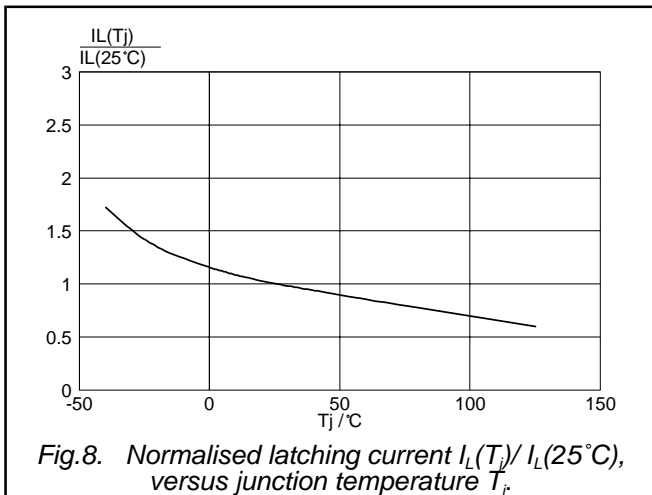
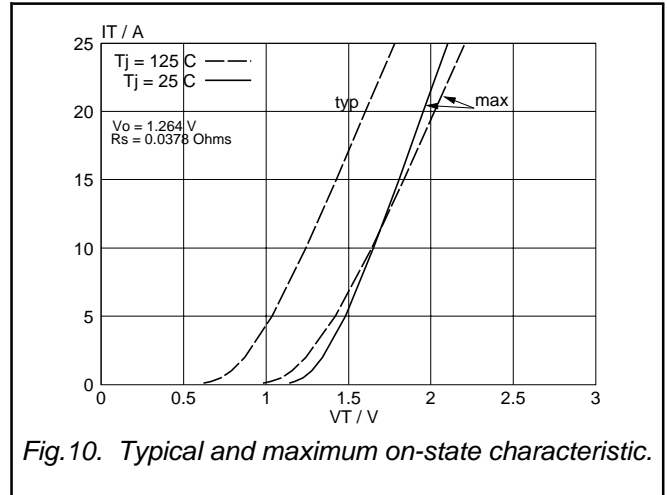
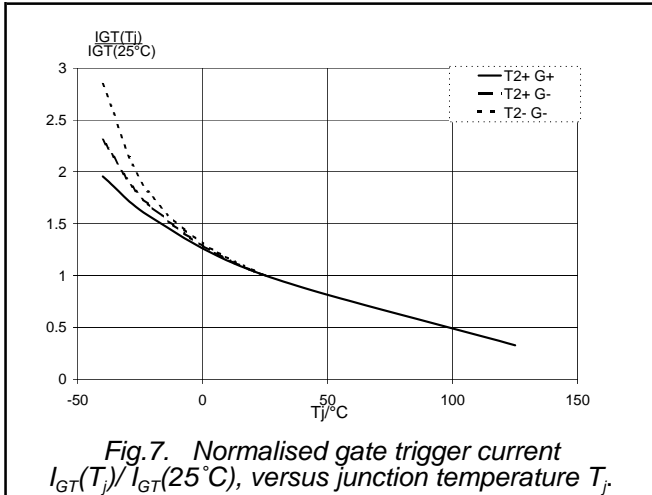


Fig. 6. Normalised gate trigger voltage $V_{GT}(T_j) / V_{GT}(25^\circ C)$, versus junction temperature T_j .

Three quadrant triacs
guaranteed commutation

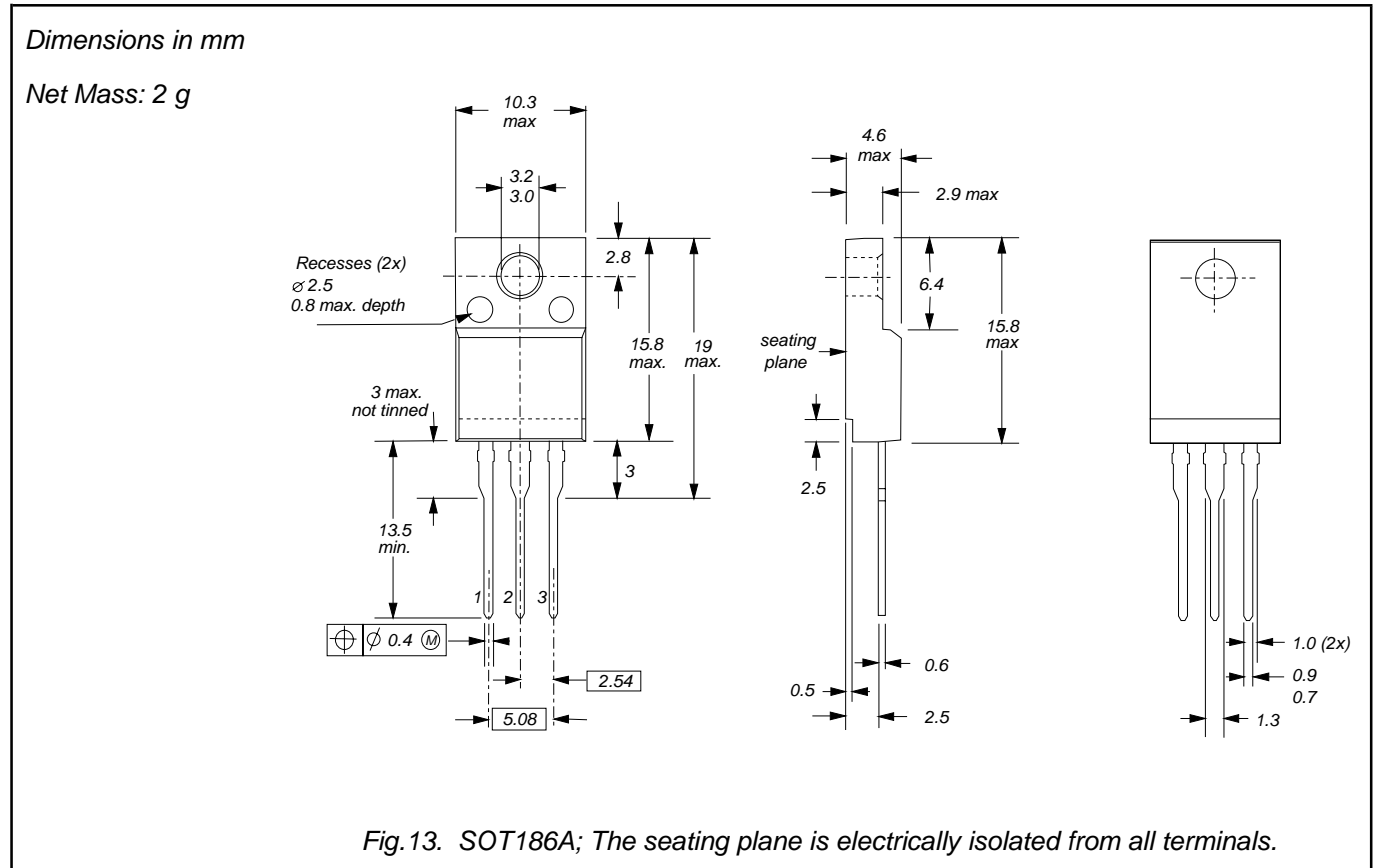
BTA208X series D, E and F



Three quadrant triacs
guaranteed commutation

BTA208X series D, E and F

MECHANICAL DATA



Notes

1. Refer to mounting instructions for F-pack envelopes.
2. Epoxy meets UL94 V0 at 1/8".

Three quadrant triacs guaranteed commutation

BTA208X series D, E and F

DEFINITIONS

DATA SHEET STATUS		
DATA SHEET STATUS³	PRODUCT STATUS⁴	DEFINITIONS
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice
Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product
Product data	Production	This data sheet contains data from the product specification. Philips Semiconductors reserves the right to make changes at any time in order to improve the design, manufacturing and supply. Changes will be communicated according to the Customer Product/Process Change Notification (CPCN) procedure SNW-SQ-650A
Limiting values		
Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.		
Application information		
Where application information is given, it is advisory and does not form part of the specification.		
© Philips Electronics N.V. 2002		
All rights are reserved. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner.		
The information presented in this document does not form part of any quotation or contract, it is believed to be accurate and reliable and may be changed without notice. No liability will be accepted by the publisher for any consequence of its use. Publication thereof does not convey nor imply any license under patent or other industrial or intellectual property rights.		

LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.

³ Please consult the most recently issued datasheet before initiating or completing a design.

⁴ The product status of the device(s) described in this datasheet may have changed since this datasheet was published. The latest information is available on the Internet at URL <http://www.semiconductors.philips.com>.